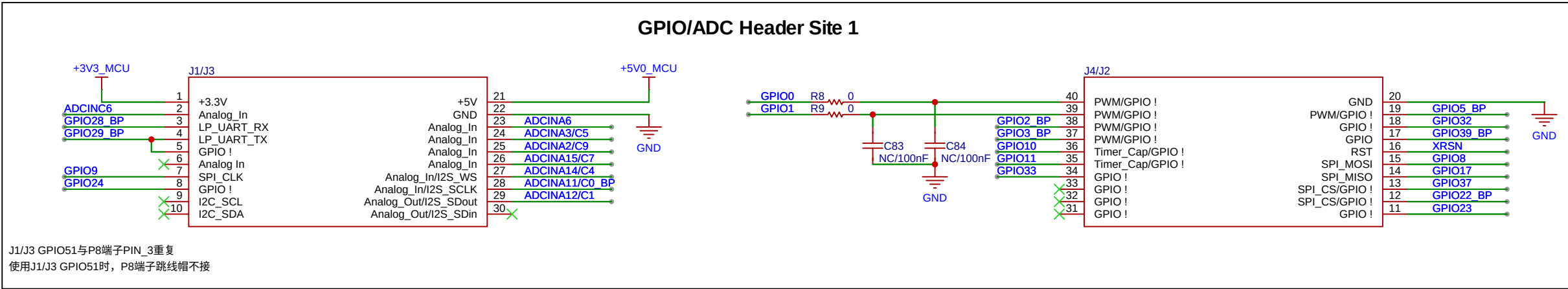
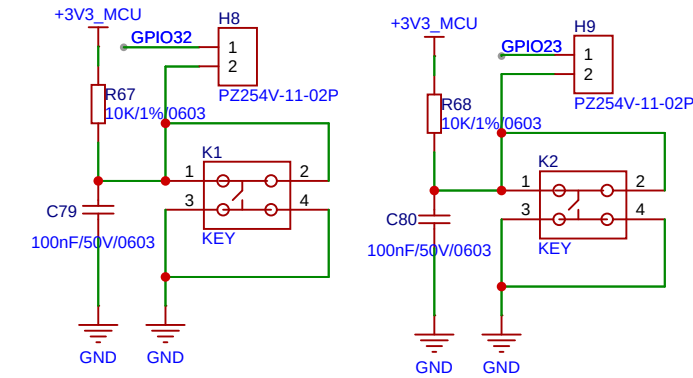


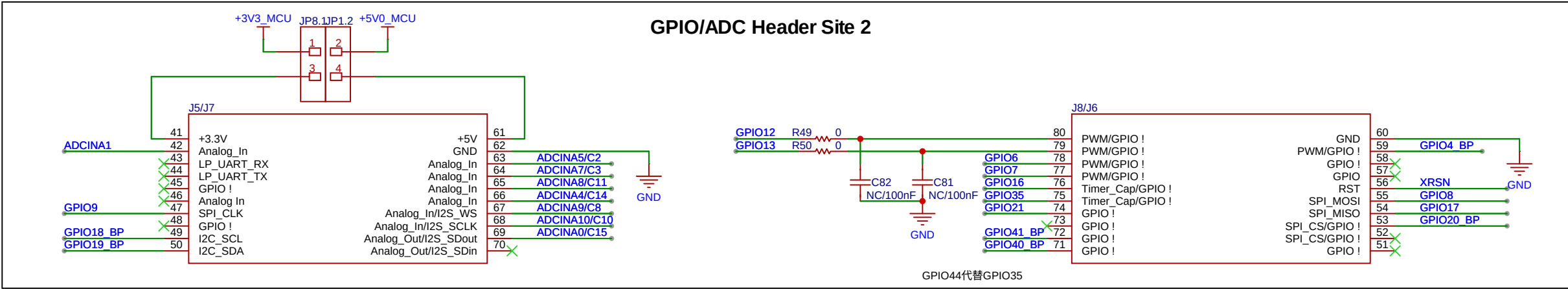
A



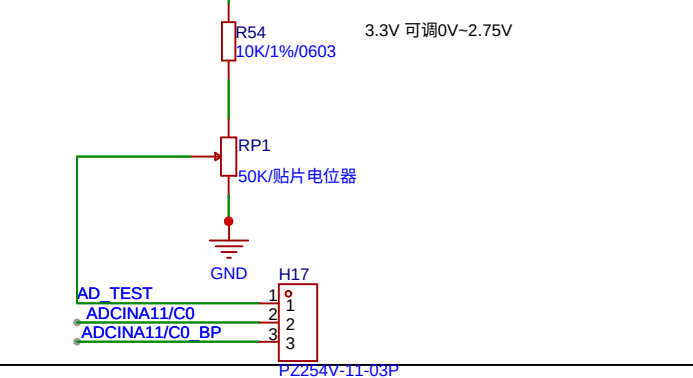
KEY



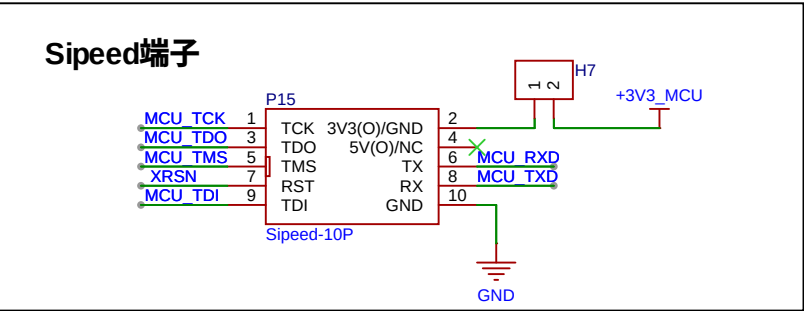
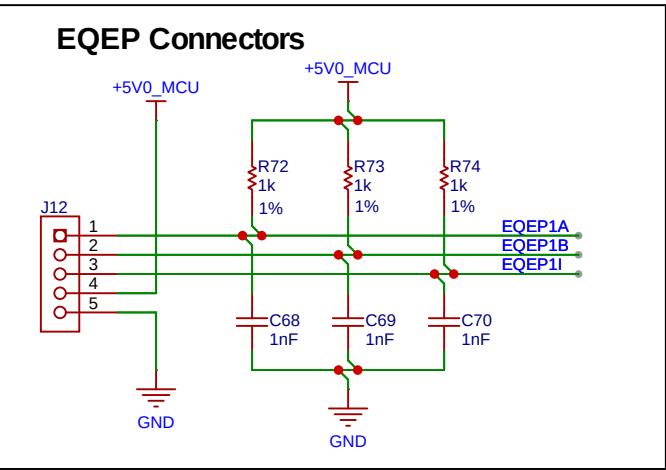
B



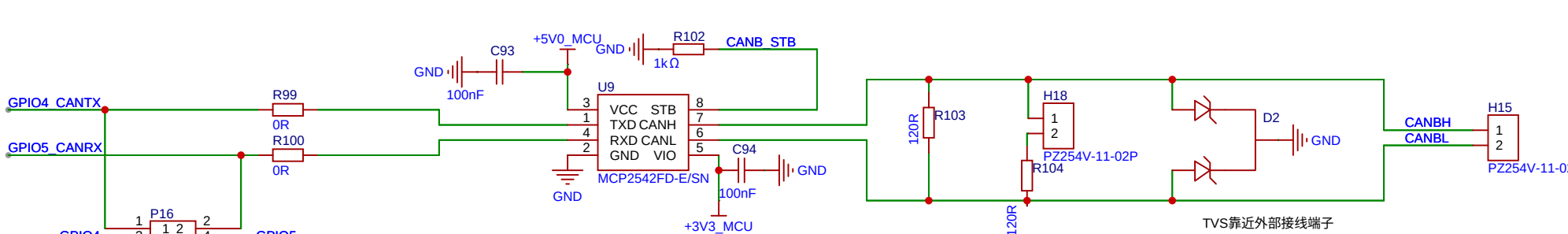
ADC_TEST



C



CAN-FD



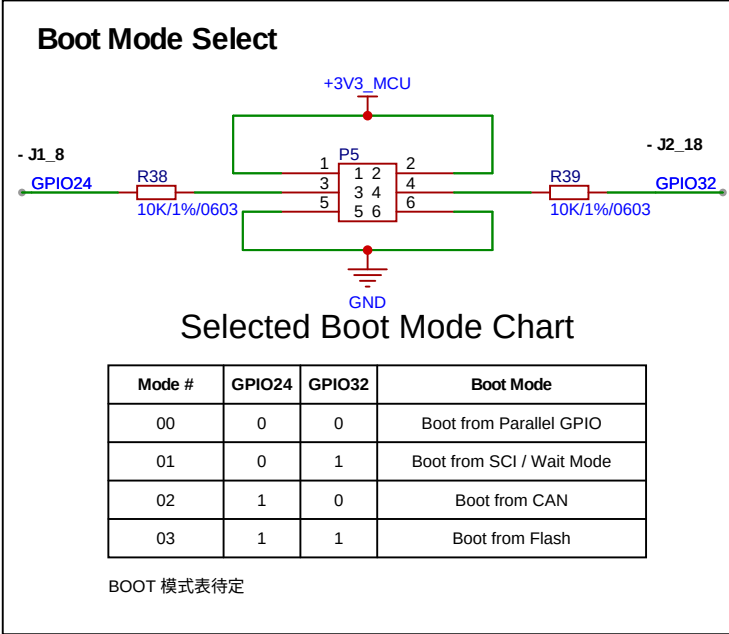
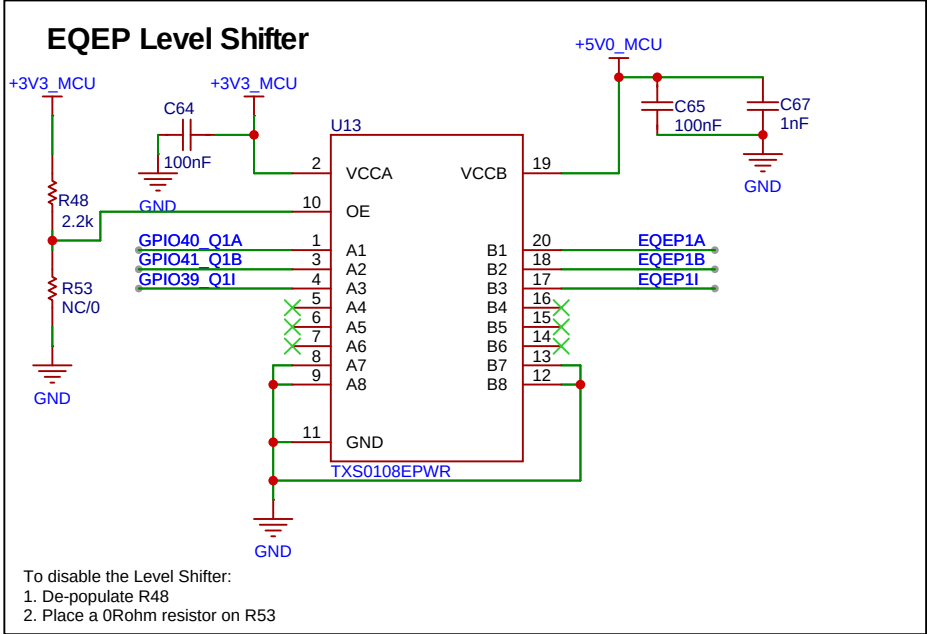
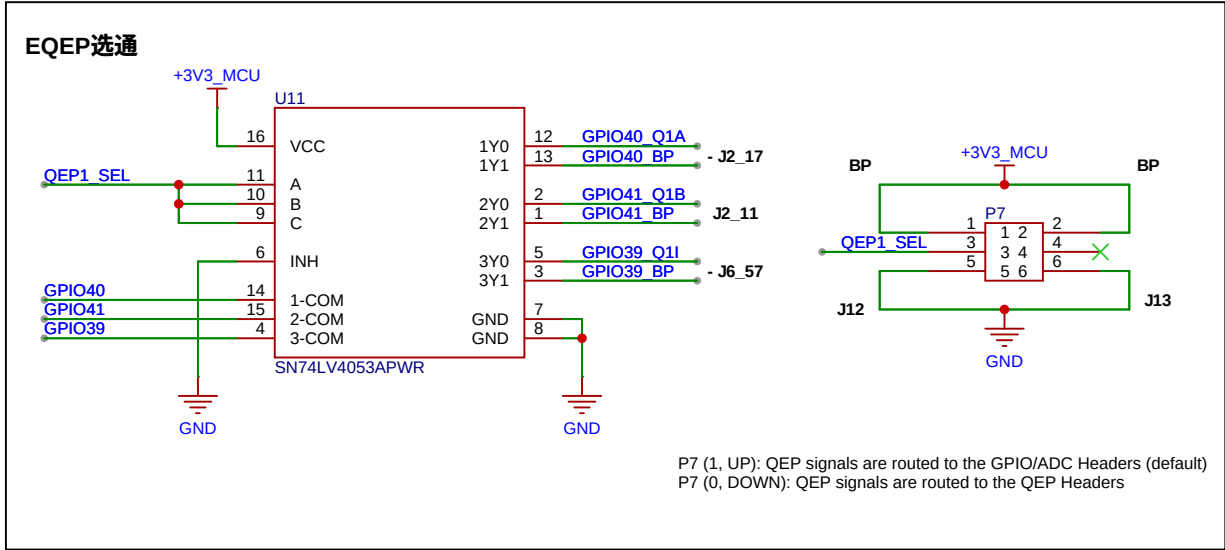
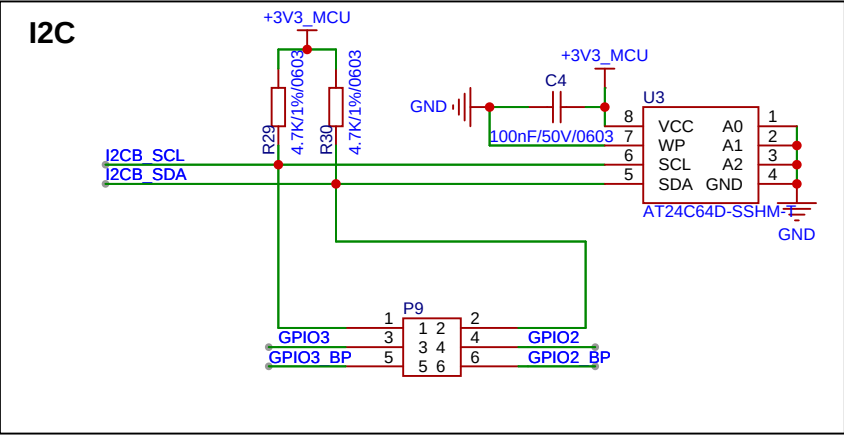
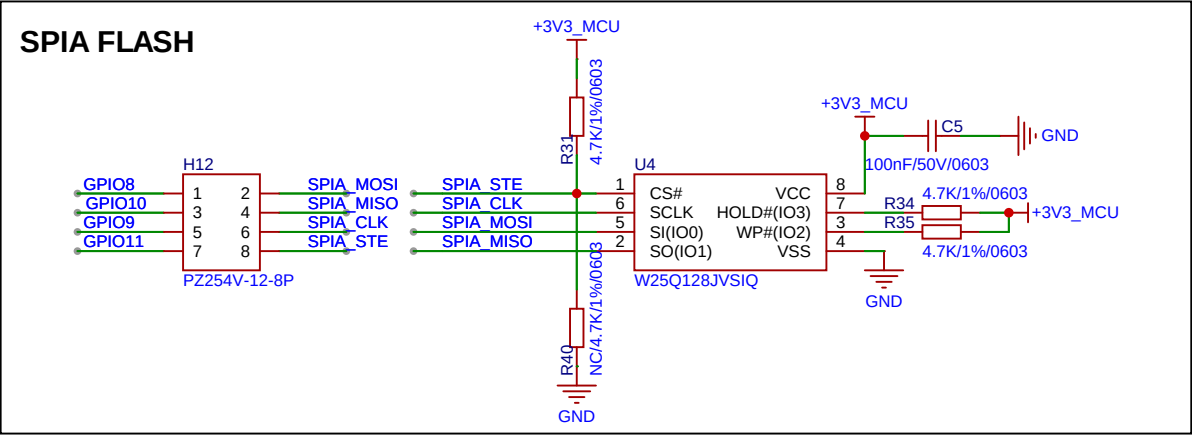
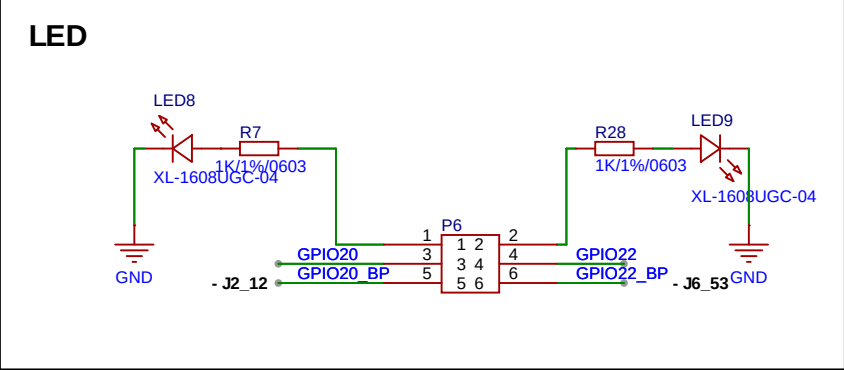
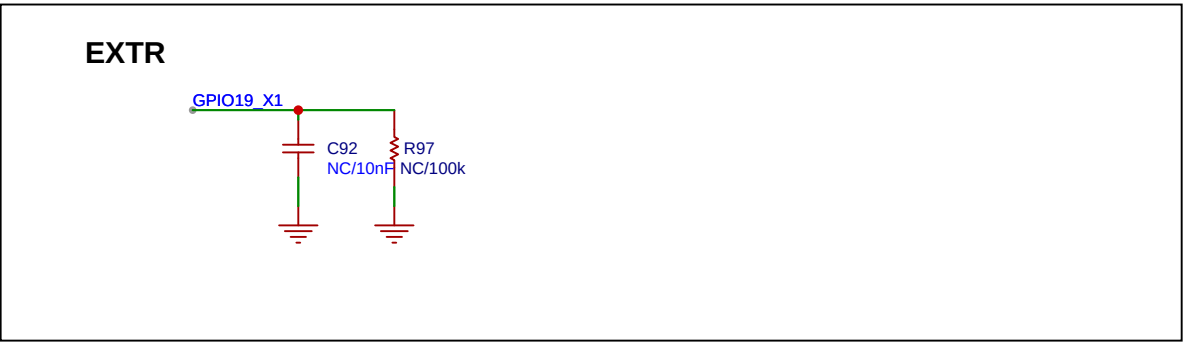
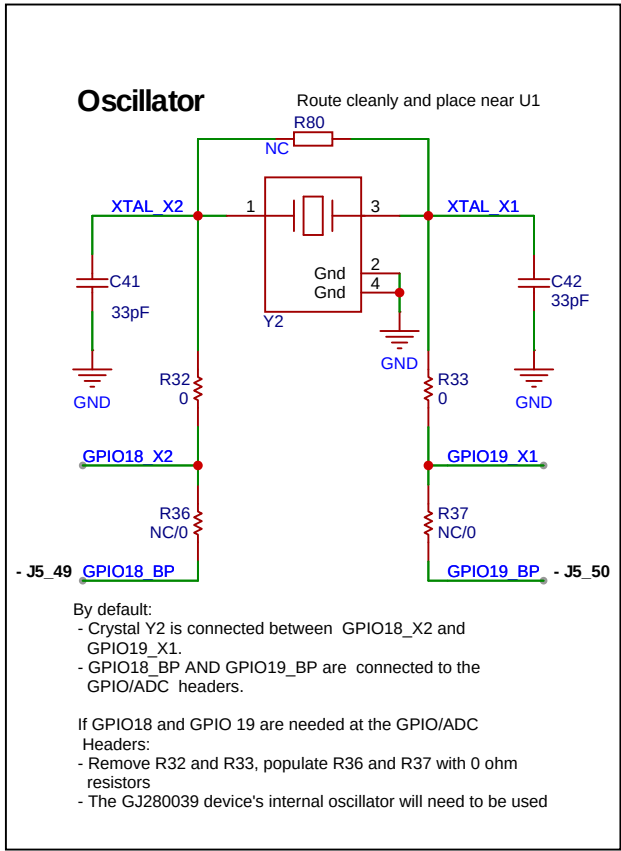
Orderable:	EVM_orderable
TID #:	TID
Number:	PRJ_Number
SVN Rev:	Rev:
Drawn By:	
Engineer:	PRJ_Engineer

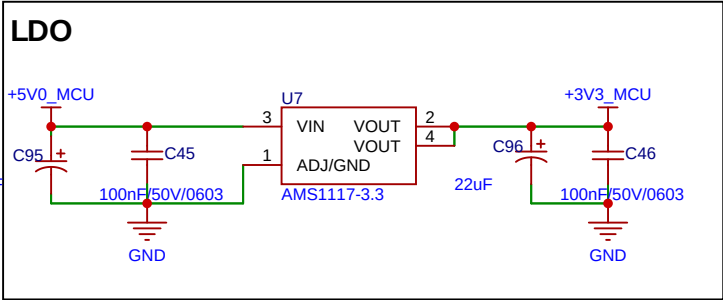
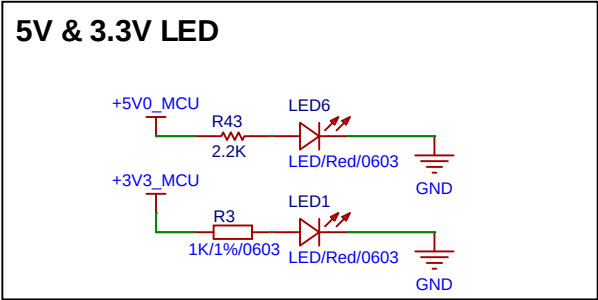
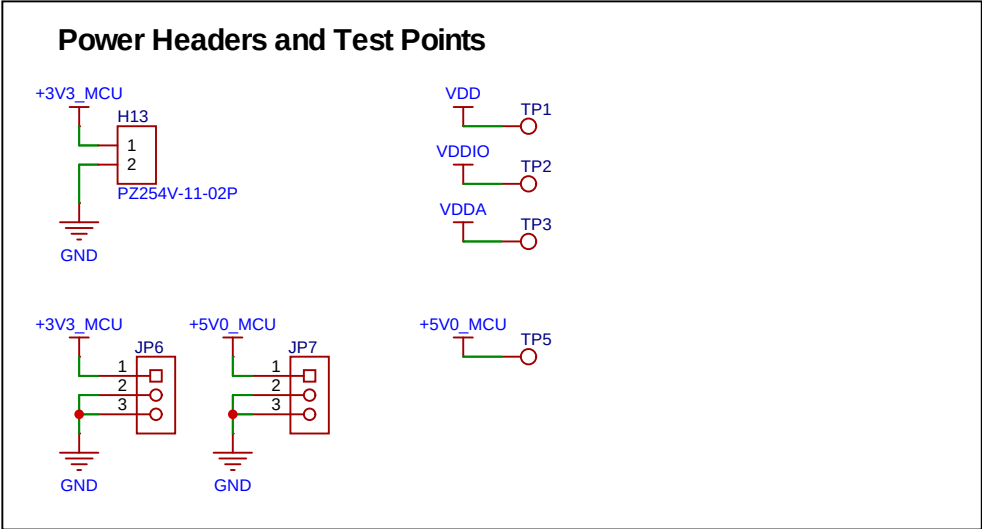
A

B

C

D





Orderable:	EVM_orderable	
TID #:	TID	
Number:	PRJ_Number	Rev:
SVN Rev:		
Drawn By:		
Engineer:	PRJ_Engineer	