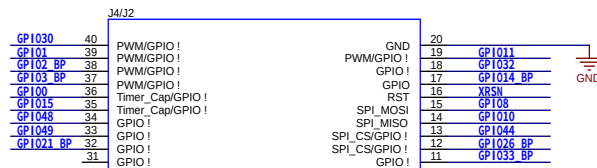
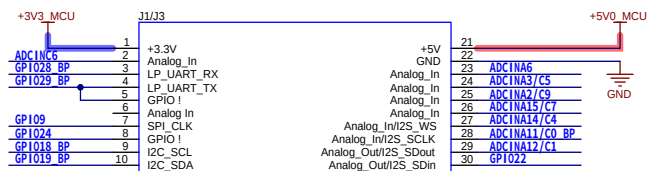
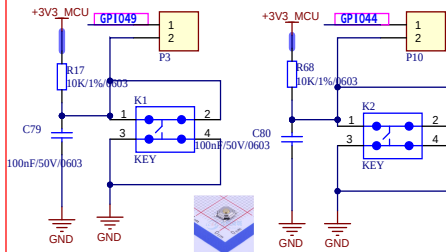


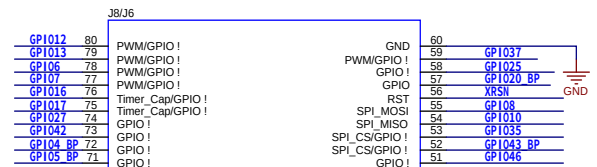
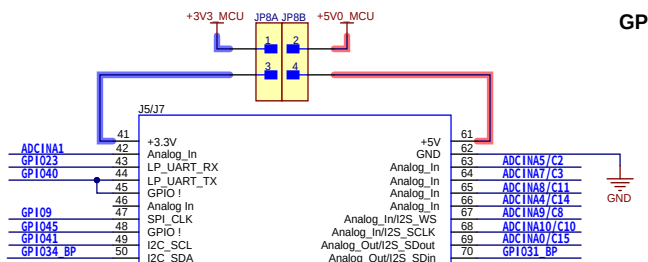
GPIO/ADC Header Site 1



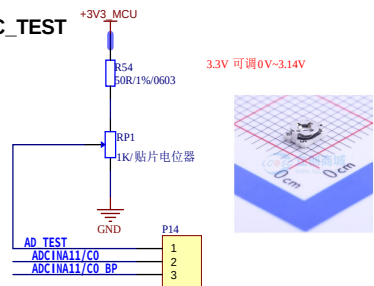
KEY



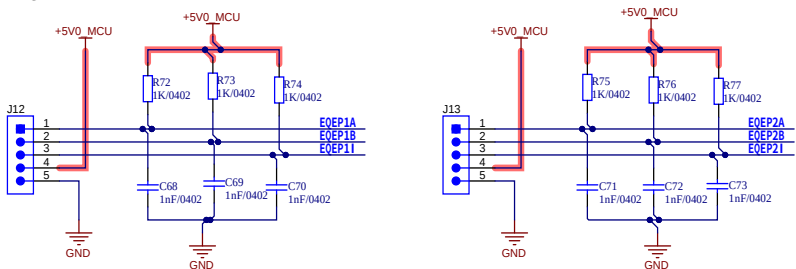
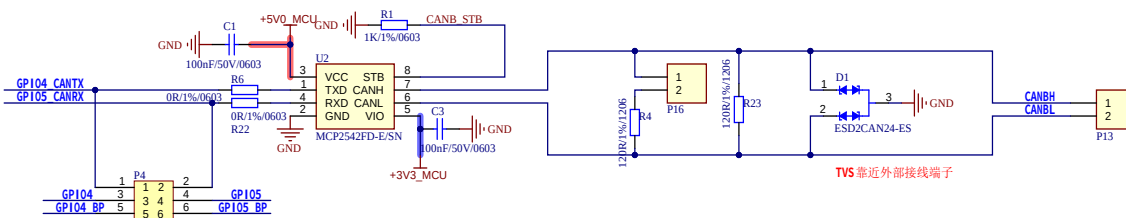
GPIO/ADC Header Site 2



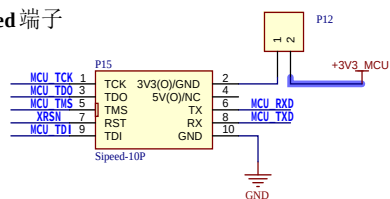
ADC_TEST



EQEP Connectors

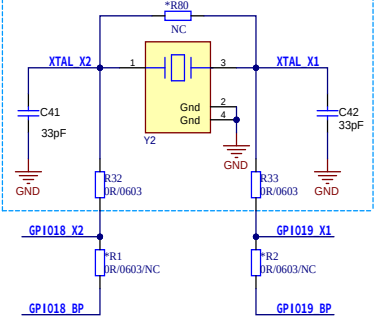
**CAN-FD**

Sipeed 端子



Title			3.0_Xplore_00157_Connectors		
Size	Number				Revision
C	1				
Date:	8/08/2025			Sheet of	
File:	D:\Desktop\13.0_Xplorer-GS32F001577\00157_Connectors.SchDoc				
	2				1

Oscillator



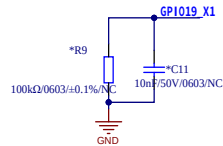
By default:

- Crystal Y2 is connected between GPIO18_X2 and GPIO19_X1.
- GPIO18_BP AND GPIO19_BP are connected to the GPIO/ADC headers.

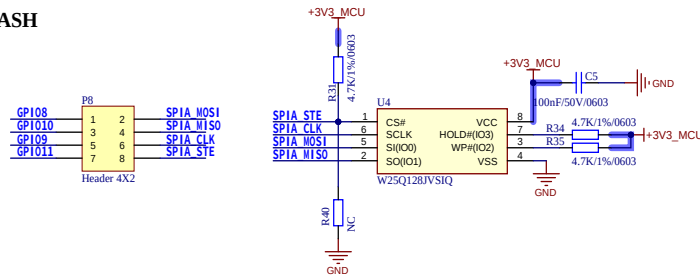
If GPIO18 and GPIO 19 are needed at the GPIO/ADC Headers:

- Remove R32 and R33, populate R36 and R37 with 0 ohm resistors
- The GJ280039 device's internal oscillator will need to be used

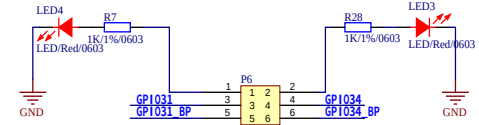
EXT_R



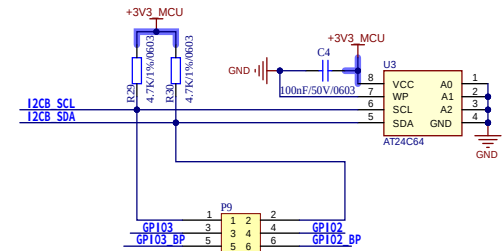
SPIA FLASH



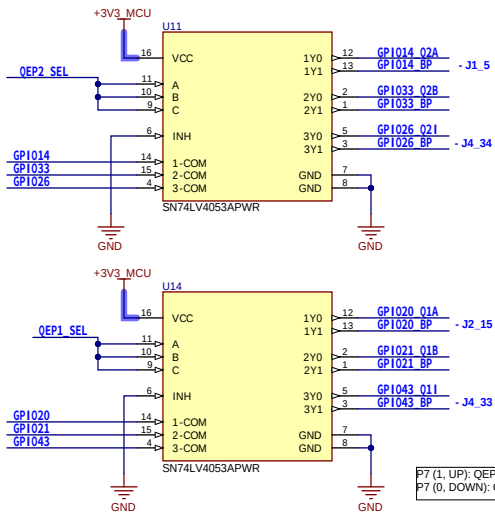
LED



I2C

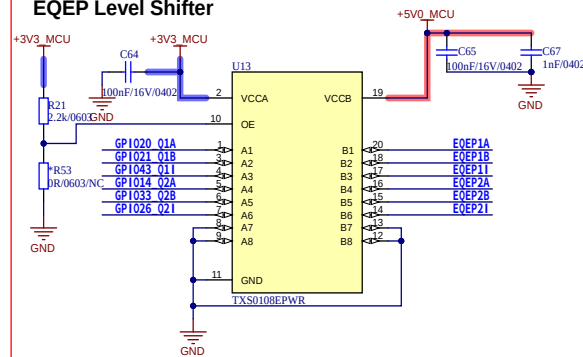


EQEP 选通



P7 (1, UP): QEP signals are routed to the GPIO/ADC Headers (default)
P7 (0, DOWN): QEP signals are routed to the QEP Headers

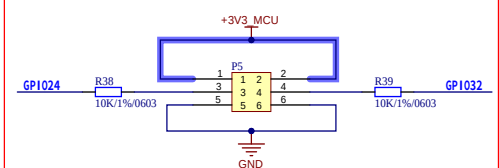
EQEP Level Shifter



To disable the Level Shifter:

1. De-populate R48
2. Place a 0Rohm resistor on R53

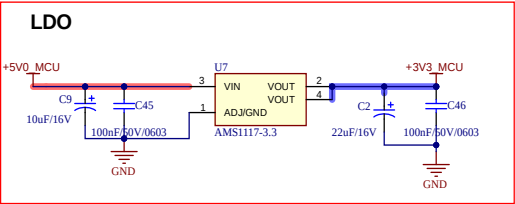
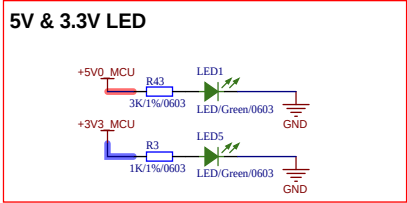
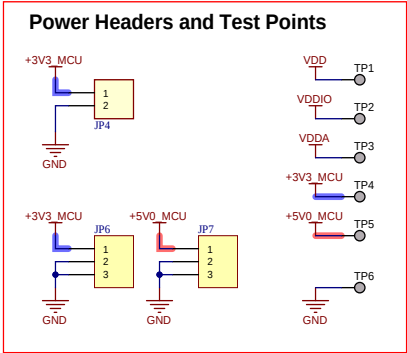
Boot Mode Select



Selected Boot Mode Chart

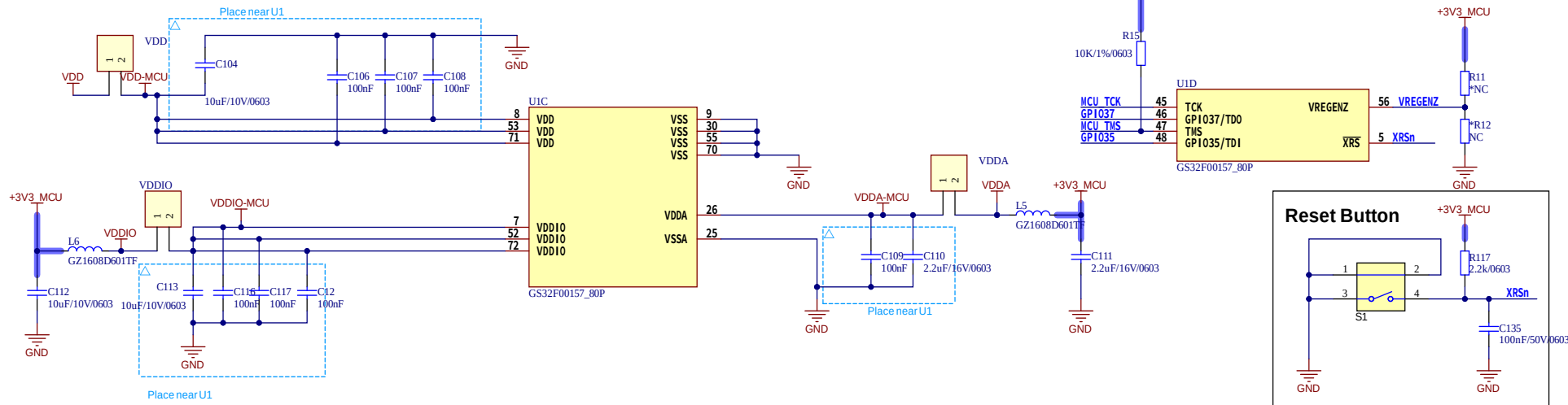
GPIO24	GPIO32	Boot Memory Space Alias
0	0	Main Flash memory is selected as boot area
0	1	GSRAM is selected as boot area
1	0	BootROM is selected as boot area
1	1	Main Flash memory is selected as boot area

Title		
3.0_Xplore_GS32F00157_Peripheral		
Size	Number	Revision
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Date:	8/08/2025	Sheet of
File:	D:\Desktop\3.0_Xplore\GS32F00157\PNQ	Release/SchDoc

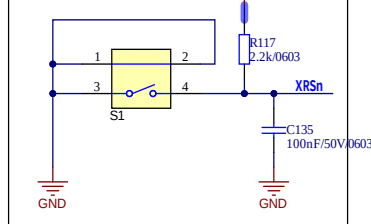


Title			3.0_Xplore_GS32F00157_Power		
Size	Number	Revision			
B	5				
Date:	8/08/2025	Sheet of			
File:	D:\Desktop\13.0_Xplore-GS32F00157\PNQ	PowerSchematicDoc			
1	2	3	4	5	6

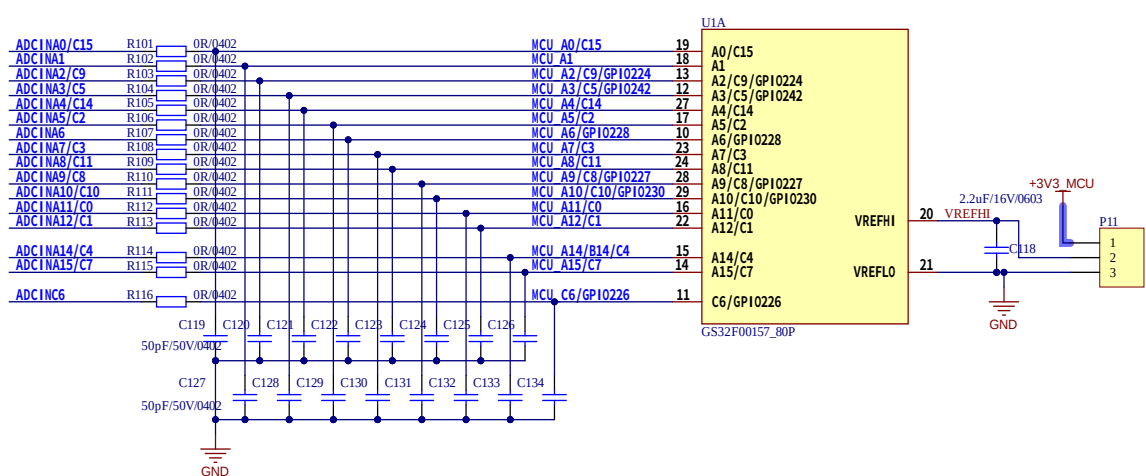
GS32F00157 Device



Reset Button

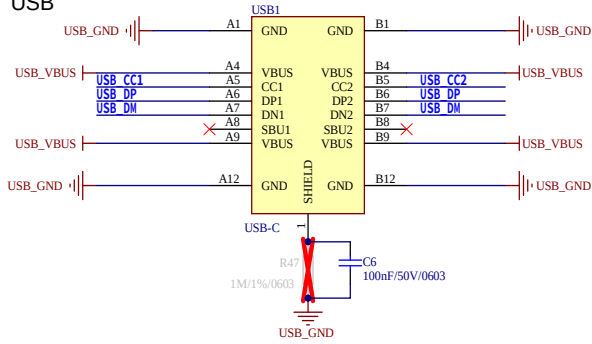


U1B		U1C		U1D	
J4_36	GP1021	34	GP1021/A18/C18	GP1022	67
J4_39	GP100	63	GP100	GP1023	65
J4_38	GP101	62	GP101	GP1024	41
J4_37	GP102	61	GP102	GP1025	42
J8_72	GP103	60	GP103	GP1026	43
J8_71	GP104	59	GP104	GP1027	44
J8_78	GP105	74	GP105	GP1028	4
J8_77	GP106	80	GP106	GP1029	3
J2_15	GP107	68	GP107	GP1030	1
J1_7	GP108	58	GP108	GP1031	2
J2_14	GP109	75	GP109	GP1032	49
J2_19	GP110	76	GP110	GP1033	38
J8_80	GP111	37	GP111	GP1034	77
J8_79	GP112	36	GP112	GP1035	64
J2_17	GP113	35	GP113	GP1036	66
J4_35	GP114	79	GP114	GP1037	57
J8_76	GP115	78	GP115	GP1038	54
J8_75	GP116	39	GP116	GP1039	69
J1_9	GP117	40	GP117	GP1040	73
J1_10	GP118	X250	GP118	GP1041	6
J6_57	GP119	X151	GP119	GP1042	31
J4_32	GP120	33	GP120	GP1043	32
				GP1044	GP1045
				GP1046	GP1047
				GP1048	GP1049

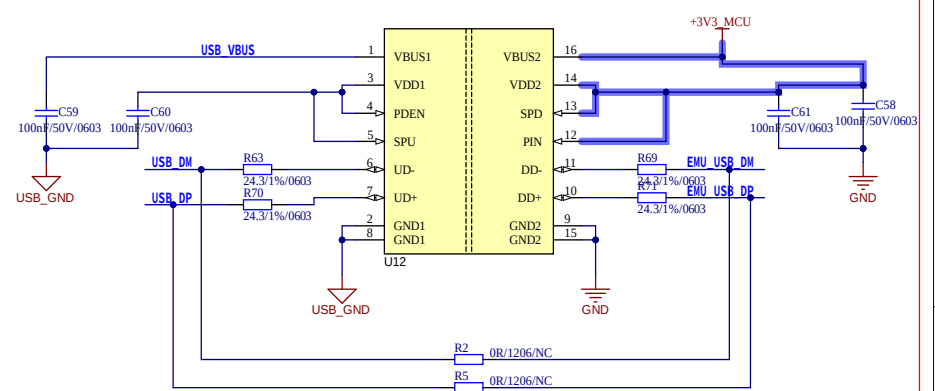
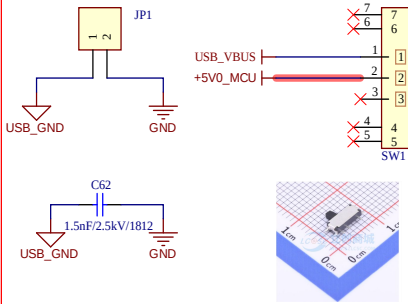


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Size	Number				Revision
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Date:	8/08/2025		Sheet of		
File:	D:\Desktop\3.0_Xplorer-GS32F00157\PCB\GS32F00157_PCB_SchDoc		NO. 1		

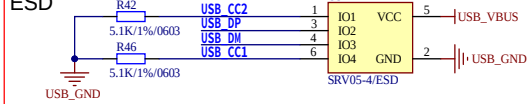
USB



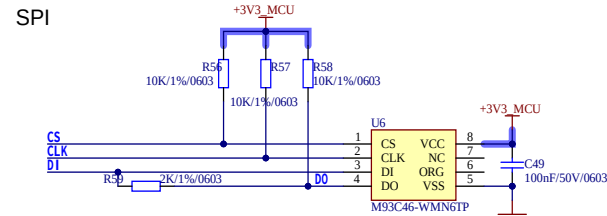
Isolation Boundary



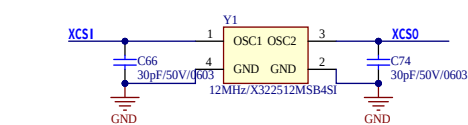
ESD



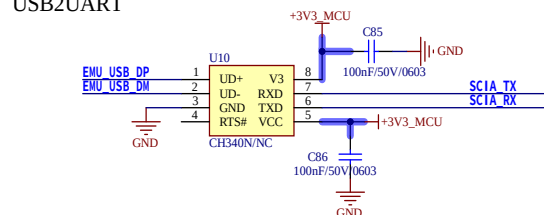
SPI



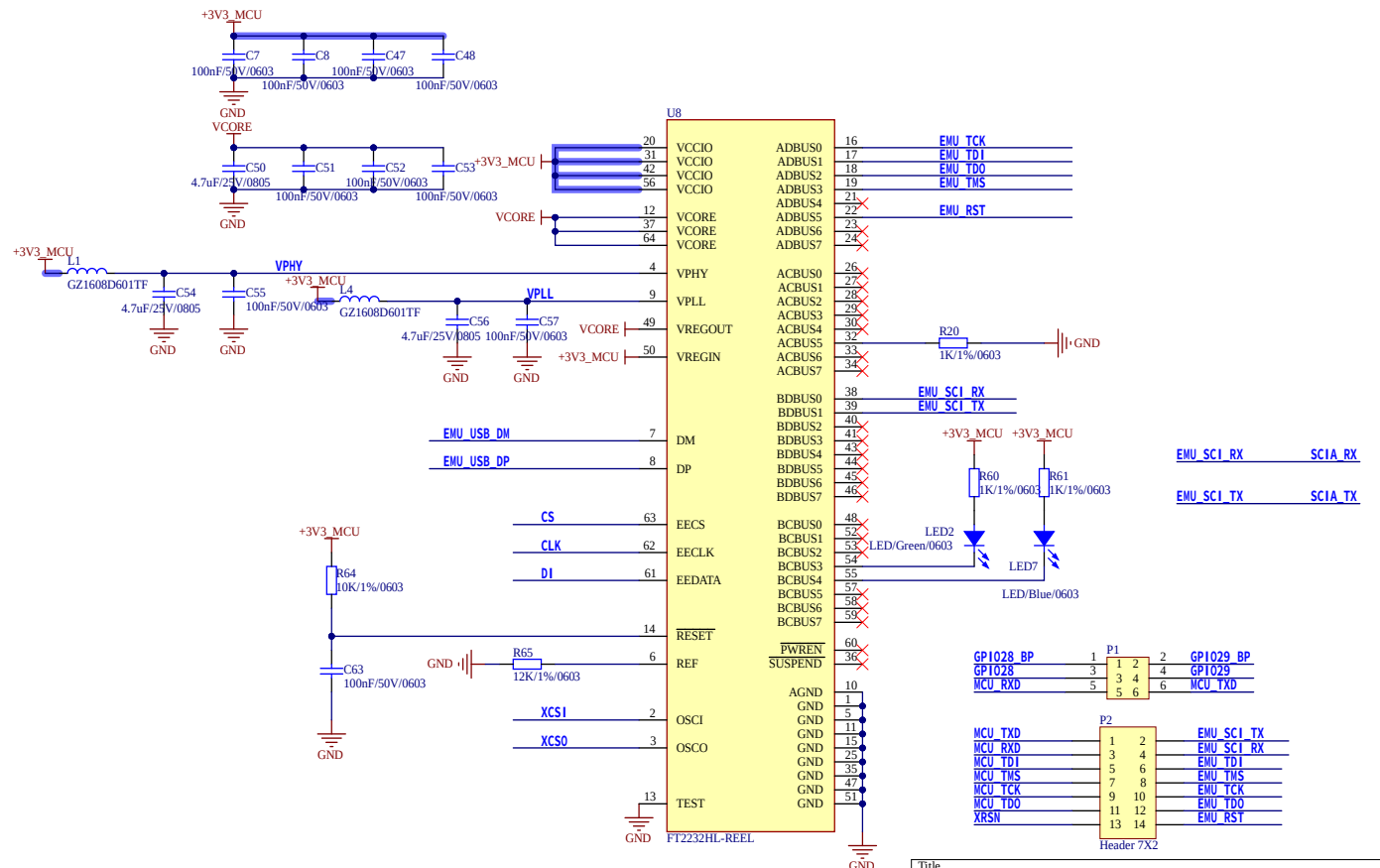
Oscillator



USB2UART



与板载调试器使用同一组串口， U10与U8 BOM 选焊



Title			3.0_Xplorer_GS32F00157_FT232		
Size	Number			Revision	
A3	4				
Date:	8/08/2025	Sheet of			
File:	D:\Desktop\...3.0_Xplorer-GS32F00157PN	Drawn By		ShDoc	

